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Professor A. Manikas holds the Chair of Communications & Array Processing in the Department of Electrical & Electronic Engineering, Imperial College London and he is currently the Technical Chair/Lead of the University Defence Research Centre in Signal Processing (DSTL/EP SRC). He is on the editorial board of IET Proceedings Signal Processing and has held a number of research consultancies

for the EU, industry and government organisations. Also, he has had various technical chairs at international conferences, has been a TPC member of major IEEE conferences. Professor Manikas is a Senior Member of IEEE and a Fellow of IET.



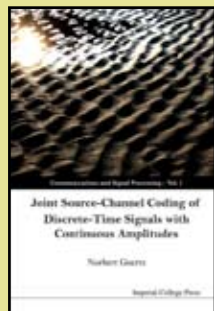
Professor A.G. Constantinides holds the chair in Signal Processing in the Department of Electrical & Electronic Engineering, Imperial College London and he is the Director of the University Defence Research Centre in Signal Processing (DSTL/EP SRC). He has been actively involved with research in various aspects of digital filter design, digital signal processing, and communications for more than 30 years. He is a Fellow of the Institute of Electrical and Electronics Engineers

(USA) and of the Institution of Electrical Engineers (UK), as well as of the Royal Society of Engineering. He holds honorary doctorates from European and Far Eastern Universities, several Visiting Professorships, Distinguished Lectureships, Fellowships and other honours around the world.

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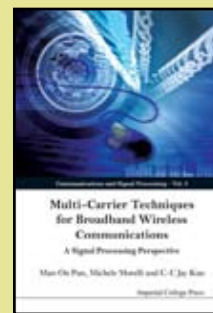
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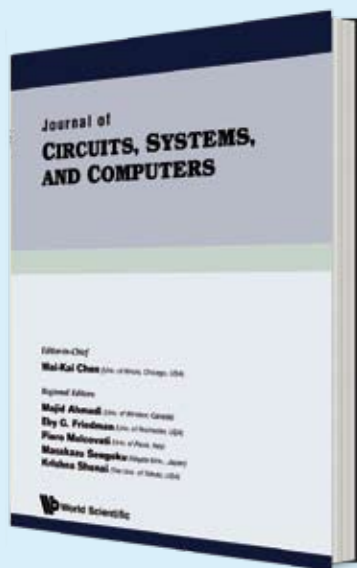
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